AMENDMENTS TO THE CLAIMS

1.   (Original) A method for preventing matching of prospective entries with table entries stored in a fully associative table, the method comprising the steps of:

writing illegal values to substantially all of said table entries in said fully associative table; and

prohibiting said prospective entries from having said illegal values under normal program execution conditions, thereby preventing any matching conditions between said table entries and said prospective entries.

2.   (Original) The method of claim 1 wherein said writing step is performed during power up of a system.

3.   (Original) The method of claim 1 wherein said writing step is initiated by executing a specific machine specific instruction.

4.   (Cancelled)

5.   (Cancelled)

6.   (Original) The method of claim 1 wherein said fully associative table is included in a system for finding and validating a most recent advanced load instruction for a given check instruction.

7.   (Original) The method of claim 1 comprising the further step of:
updating entries in a fully associative table employing a pointer to indicate a first table location containing an invalid entry.

8.   (Original) The method of claim 1 comprising the further step of:
storing memory addresses in said fully associative table.

9.   (Original) The method of claim 1 comprising the further step of:
storing register numbers in said fully associative table.

10. (Original) The method of claim 1 wherein said writing step comprises the step of:

issuing a force update command, thereby causing a plurality of presettable storage elements in said fully associative table to acquire a predetermined illegal value.

11. (Original) A system for preventing matching of prospective entries with table entries stored in a fully associative table, the system comprising:

means for writing illegal values to substantially all of said table entries in said fully associative table; and

means for prohibiting said prospective entries from having said illegal values, thereby preventing any matching conditions between said table entries and said prospective entries.

12. (Original) The system of claim 11 wherein said writing means operates during power up of a system.

13. (Original) The system of claim 11 wherein said writing means is activated by executing a specific machine specific instruction.

14. (Cancelled)

15. (Cancelled)

16. (Original) The system of claim 11 wherein said fully associative table is included in a system for finding and validating a most recent advanced load instruction for a given check instruction.

17. (Original) The system of claim 11 further comprising:

means for updating entries in a fully associative table employing a pointer to indicate a first table location containing an invalid entry.

18. (Original) The system of claim 11 further comprising:

means for storing memory addresses in said fully associative table.

19.   (Original) A system for disabling matching of prospective entries with entries resident in an fully associative table, the system comprising:

a plurality of entry locations in said fully associative table; and

a force update command for causing said plurality of entry locations to acquire predetermined bit values not present in prospective entries at ports connected to said fully associative table.

20.   (Cancelled)